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09/888,689	06/25/2001	Phillip E. Byrd	4715US (00-1057)	1027	
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TRASK BRITT		EXAMINER			
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SALT LAKE CITY, UT 84110			CHAN, E		
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			2829		
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Please find below and/or attached an Office communication concerning this application or proceeding.

Application No.								
Examiner emily y chan 2229 -7.1: MAILING DATE of this communication app are on the cover sheet with the correspondence address Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) FROM THE MAILING DATE of THIS COMMUNICATION. Educations of time may be available under the protestions of 37 CPR 1.136(a). In no event, however, may a reply to timely filled If the point of reply is general advance is been than thing (00) days, a reply which the datulatory minimum of thing (20) days will be considered timely. If NO period for reply is general advance is been than thing (00) days, and write expens SK (8) MXINTE from the mailing date of this communication is the protection of the drawing(a) be held in aboyance. See 37 CFR 1.65(a). It is proved, corrected drawings are required in reply to this Office action. It is proved, corrected drawings are required in reply to this Office action. It is proved, corrected drawings are required in reply to this Office a		Application No.	Applicant(s)					
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THE MAILING DATE OF THIS COMMUNICATION. Extensions of time may be variable under the provision of 3 CPR 1.15(q). In no event, however, may a reply be timely filed after SX (6) MONTHS from the mailing date of this communication. It (1) Eventor to reply substitute and advanced to the communication. It (1) Eventor to reply substitute the set of extended period for reply will, by attacker, cause the application to become ARANDONED (39 U.S.C. § 133). Any reply received by the Office and the time there here mornicated patent term adjustment. See 37 CPR 1.704(b). Status 1) Responsive to communication(s) filed on 12-10-01. 2a) This action is FINAL. 2b) This action is rivated them provide the set of the communication, even if timely filed, may reduce any considerated term adjustment. See 37 CPR 1.704(b). Status 1) Responsive to communication(s) filed on 12-10-01. 2a) This action is FINAL. 2b) This action is rivated them adjustment is a consideration. 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213. Disposition of Claims 4) Claim(s) 1-55 is/are pending in the application. 4a) Of the above claim(s) is/are allowed. 6) Claim(s) is/are allowed. 6) Claim(s) is/are allowed. 7) Claim(s) is/are allowed. 8) Claim(s) 1-55 are subject to restriction and/or election requirement. Application Papers 9) The specification is objected to by the Examiner. 10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. 11) The proposed drawing correction filed on is/are: a) approved b) disapproved by the Examiner. 12) The eath or declaration is objected to by the Examiner. 12) The eath or declaration is objected to by the Examiner. 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). 13) Acknowledgment is made of a claim for domestic priority documents have been received. 14) Acknowledgment is made		ars on the cover she	et with the correspondence address					
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Application/Control Number: 09/888,689

Art Unit: 2829

Election/Restrictions

The inventions are distinct, each from the other because:

- Claims 1-21 are, drawn to a method of manufacturing a probe card, classified in class 29, subclass825.
- II. Claims 22-25, drawn to a method of using a probe for testing a least one semiconductor die, classified in class 324, subclass765.
- III. Claims26-49, drawn to a probe card, classified in class257, subclass 48.
- IV. Claims 50-55, drawn to a semiconductor die testing system, classified in class 324, subclass754.

Inventions I and II. are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately usable. In the instant case, invention 2 has separate utility such as a probe card which is used for testing at *least one semiconductor die* by supplying test signals to the at least one *semiconductor die* through a fuse of the plurality of fuses. See MPEP § 806.05(d).

Inventions IV and III are related as combination and subcombination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP §

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806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination such as claimed at least one fuse of the plurality of fuses which is configured to be replaceable or repairable after being tripped by an electrical current supplied thereto. The subcombination has separate utility such as it is not used to a semiconductor die testing system

Inventions I and III are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case the product does not required that at least one fuse element of the plurality of fuse elements which is formed of a material selected from the group consisting of titanium tungsten, aluminum, platinum, silicide, copper, nichrome, doped polysilicon, metal silicide, and allays of any thereof.

Inventions III. and II are related as product and process of use. The inventions can be shown to be distinct if either or both of the following can be shown: (1) the process for using the product as claimed can be practiced with another materially different product or (2) the product as claimed can be used in a materially different process of using that product (MPEP § 806.05(h)). In the instant case the product as claimed does not use the method of using a probe for testing a least one semiconductor die. Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

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